

Notice of Allowability	Application No.	Applicant(s)	
	10/736,719	WON, SAM KYU	
	Examiner	Art Unit	
	Long Nguyen	2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the application filed on 12/16/03.
2. ☒ The allowed claim(s) is/are 1-6.
3. ☒ The drawings filed on 16 December 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).**
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| <ol style="list-style-type: none"> 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____ |
|--|---|

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Paul Stephens on 2/22/05.

The application has been amended as follows:

In The Claims

Claim 1, line 1, "circuit, comprising:" has been changed to --circuit located inside a chip, the high voltage transfer circuit comprising:--.

Claim 1, line 3, "a chip to the" has been changed to --the chip to an--.

Claim 1, line 6, "generated outside the" has been changed to --generated from the outside of the--.

Claim 2, line 5, "the potential" has been changed to --a potential--.

Claim 2, line 6, "the potential" has been changed to --a potential--.

Claim 2, line 8, "a voltage" has been changed to --the potential--.

Claim 3, line 5, "the potential" has been changed to --a potential--.

Claim 3, line 6, "the potential" has been changed to --a potential--.

Claim 3, line 8, "a voltage" has been changed to --the potential--.

Claim 3, line 10, "generated outside the" has been changed to --generated from the outside of the--.

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Claim 4, line 4, "circuit;" has been changed to --circuit of the high voltage transfer circuit;--

Claim 4, line 6, "the outside;" has been changed to --an outside of the high voltage transfer circuit;--.

Claim 4, line 11, "outside to an internal circuit" has been changed to --outside of the high voltage transfer circuit to the internal circuit--.

Claim 5, line 6, "within the chip" has been changed to --from the pumping circuit --.

Claim 5, line 6, "the potential" has been changed to --a potential--.

Claim 5, line 7, "the potential" has been changed to --a potential--.

Claim 5, line 9, "a voltage" has been changed to --the potential--.

Claim 5, line 12, "within the chip" has been changed to --from the pumping circuit--.

Claim 5, line 12, second occurrence "chip" has been changed to --high voltage transfer circuit--.

Claim 6, line 6, "outside the chip" has been changed to --from the outside of the high voltage transfer circuit--.

Claim 6, line 6, "the potential" has been changed to --a potential--.

Claim 6, line 7, "the potential" has been changed to --a potential--.

Claim 6, line 9, "a voltage" has been changed to --the potential--.

Claim 6, line 12, "outside the chip to the inside of the chip" has been changed to --from the outside of the high voltage transfer circuit to the inside of the high voltage transfer circuit--.

Reasons For The Above Changes

The above changes have been made to correct the minor informalities in the claims so that the claims are clear.

REASONS FOR ALLOWANCE

2. Claims 1-6 are allowed.

Claim 1 is allowed because the prior art of record fails to disclose or suggest a high voltage transfer circuit located inside a chip, wherein the high voltage transfer circuit including a first high voltage switch for transferring a high voltage generated within the chip to an outside of the chip according to a clock signal and a first control signal, and a second high voltage switch for transferring the high voltage generated from the outside of the chip to the inside of the chip according to the clock signal and a second control signal.

Claims 2 and 3 are allowed because they depend on claim 1.

Claim 4 is allowed because the prior art of record fails to disclose or suggest a high voltage transfer circuit including a pumping circuit, a high voltage pad, a first high voltage switch and a second high voltage switch with the recited connections and operations set forth therein. In particular, the prior art fails to disclose that the first high voltage switch is for transferring the high voltage generated by the pumping circuit to the high voltage pad according to a clock signal and a first control signal in a monitoring mode, and the second high voltage switch is for transferring the high voltage supplied to the high voltage pad from the outside of the high voltage transfer circuit to an internal circuit of the high voltage transfer circuit according to the clock signal and a second control signal in an external voltage supply mode.

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Claims 5 and 6 are allowed because they depend on claim 4.

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Long Nguyen whose telephone number is (571) 272-1753. The Examiner can normally be reached on Monday to Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tim Callahan, can be reached at (571) 272-1740. The fax number for this group is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

February 22, 2005



Long Nguyen
Primary Examiner
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